

REMARKS

The above preliminary amendment is made to remove multiple dependencies from claims 5, 6, 7, 8, 19, 20, 21, 27 and 56.

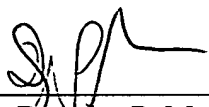
Applicants respectfully request that the preliminary amendment described herein be entered into the record prior to calculation of the filing fee and prior to examination and consideration of the above-identified application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller (Reg. No. 30,300), at (612) 371.5237.

Respectfully submitted,

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Dated: December 21, 2001

By   
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Douglas P. Mueller  
Reg. No. 30,300

DPM/tvm

## MARKED-UP COPY

5. (Amended) The display apparatus according to [any one of claim 1 to claim 4] claim 1, wherein each capacitance element of the pixel is set so a second capacitance ratio  $\alpha_{st}$  shown by (Expression 57) is substantially constant.

(Expression 57)

$$\alpha_{st} = C_{st} / C_{tot}$$

6. (Amended) The display apparatus according to [any one of claim 1 to claim 4] claim 1; wherein each capacitance element of the pixel is set so a the second capacitance ratio  $\alpha_{st}$  shown by (Expression 58) increases continuously or in stages according to the distance from the power feeding edge of the scanning electrode.

(Expression 58)

$$\alpha_{st} = C_{st} / C_{tot}$$

7. (Amended) The display apparatus according to [any one of claim 1 to claim 4] claim 1, wherein the display medium is a liquid crystal.

8. (Amended) The display apparatus according to [any one of claim 1 to claim 4] claim 1, further comprising a means for overlapping a voltage to the driving circuit of the scanning signal via the storage capacitance.

19. (Amended) The display apparatus according to [any one of claim 11 to claim 17] claim 11, wherein each capacitance element of the pixel is set as the fourth capacitance ratio  $\alpha_{st1}$  shown by (Expression 60) is substantially constant.

(Expression 60)

$$\alpha_{st1} = C_{st1} / C_{tot}$$

20. (Amended) The display apparatus according to [any one of claim 11 to claim 17] claim 11, wherein each capacitance element of the pixel is set so the fourth capacitance ratio  $\alpha_{st1}$  shown by (Expression 61) increases continuously or in stages according to the distance from the power feeding edge of the scanning electrode.

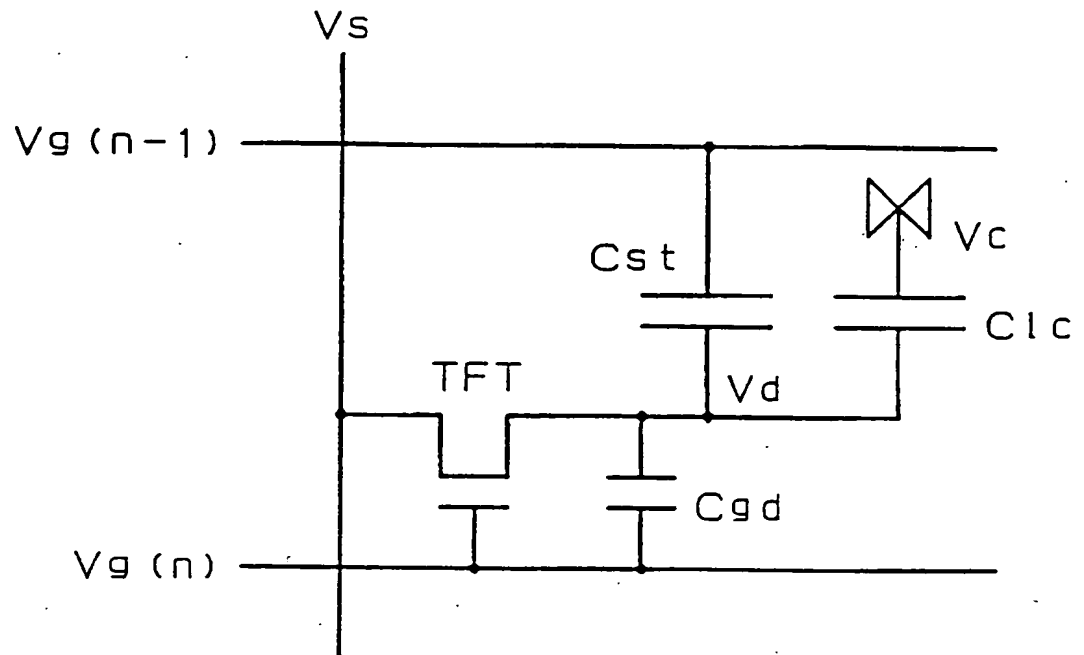
(Expression 61)

$$\alpha_{st1} = C_{st1} / C_{tot}$$

21. (Amended) The display apparatus according to [any one of claim 11 to claim 17] claim 11, wherein a parallel monotonic capacitance is not formed between the pixel electrode and the opposite electrode via the display medium.

27. (Amended) The display apparatus according to [any one of claim 11 to claim 17] claim 11, further comprising a means for overlapping a voltage to the driving circuit of the scanning signal via the storage capacitance.

56. (Amended) The display apparatus according to [any one of claims 54 and 55] claim 54, wherein the gate pulse is applied to more than two gate wirings at the same time.



— (PRIOR ART) —

FIG. 34

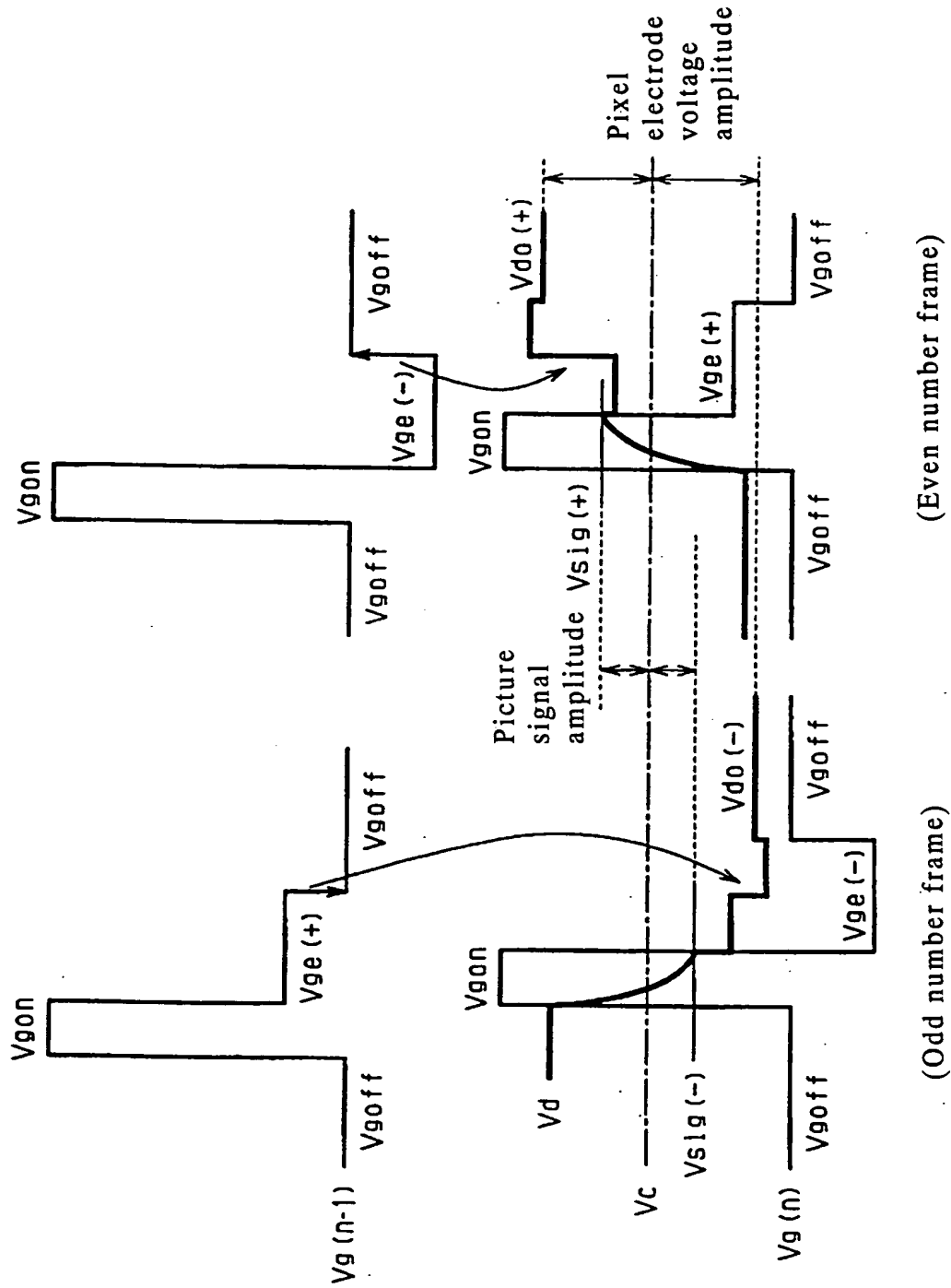


FIG. 35 -- (PRIOR ART) --

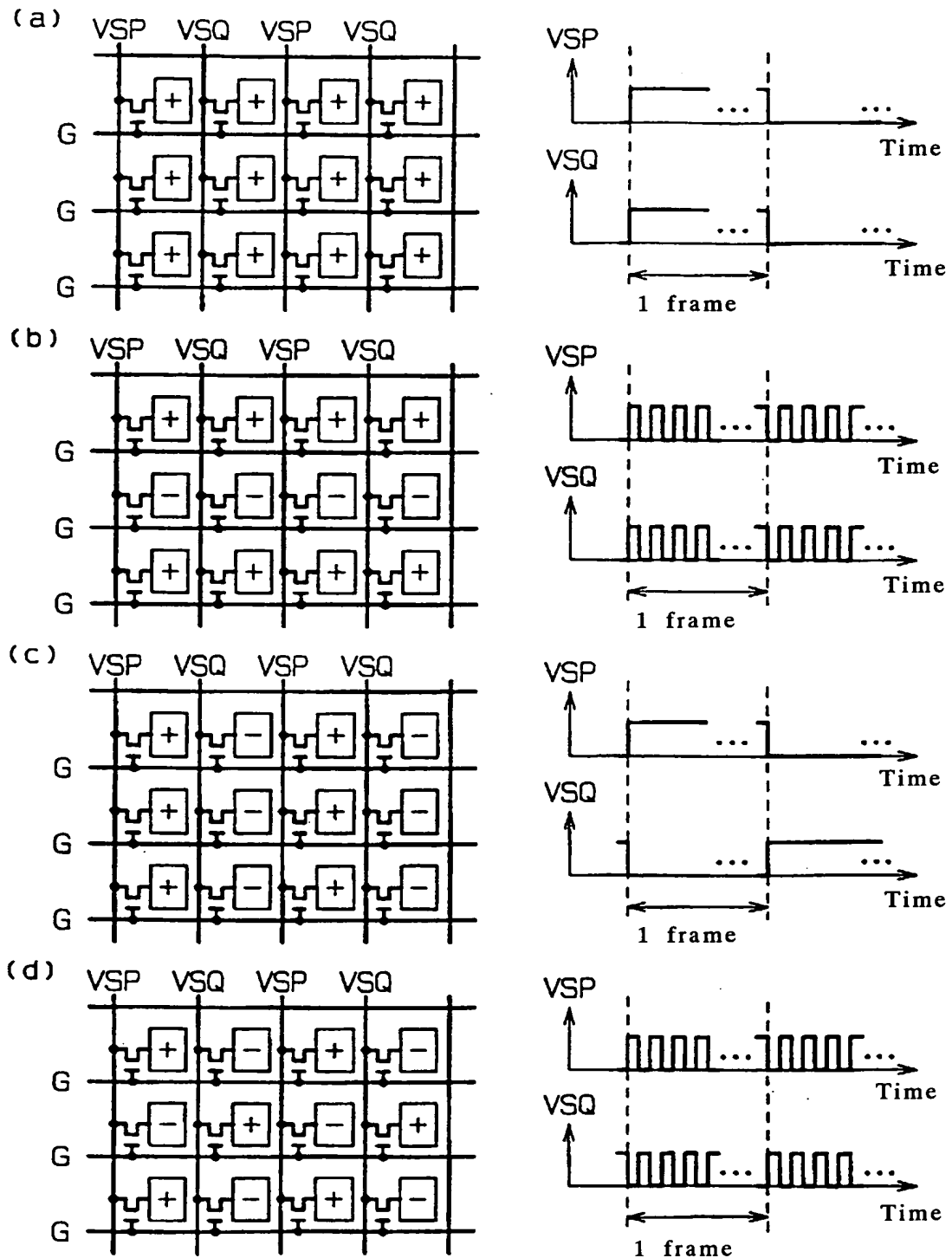
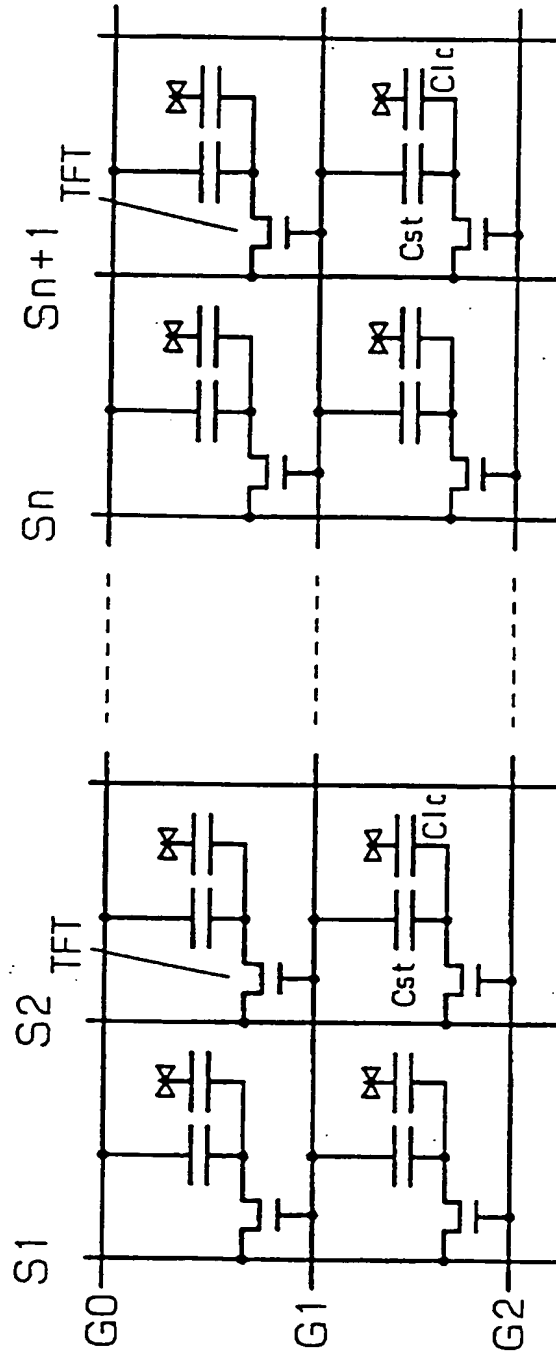


FIG. 36 - (PRIOR ART) -



-- (PRIOR ART) --

FIG. 37

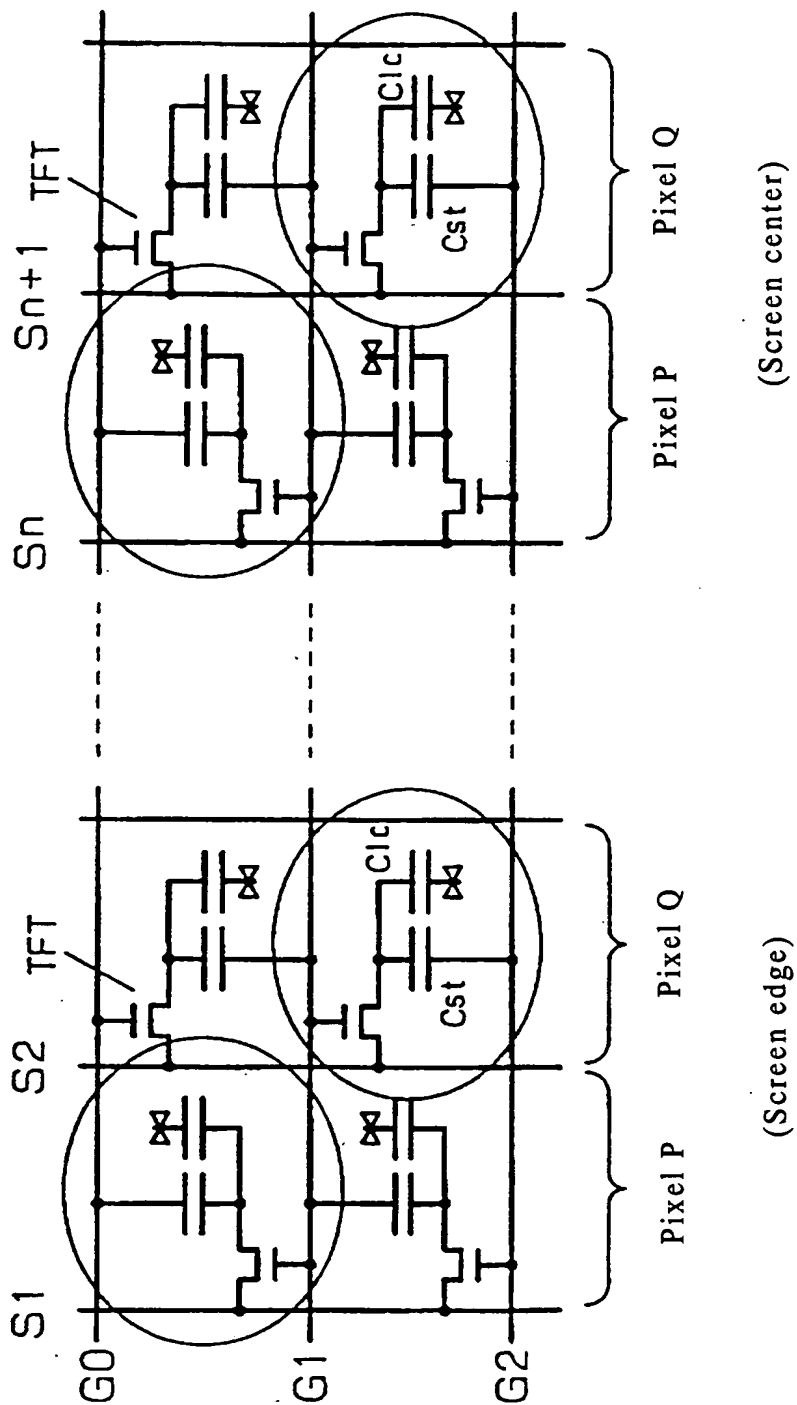


FIG. 38 --(PRIOR ART)--



FIG. 39

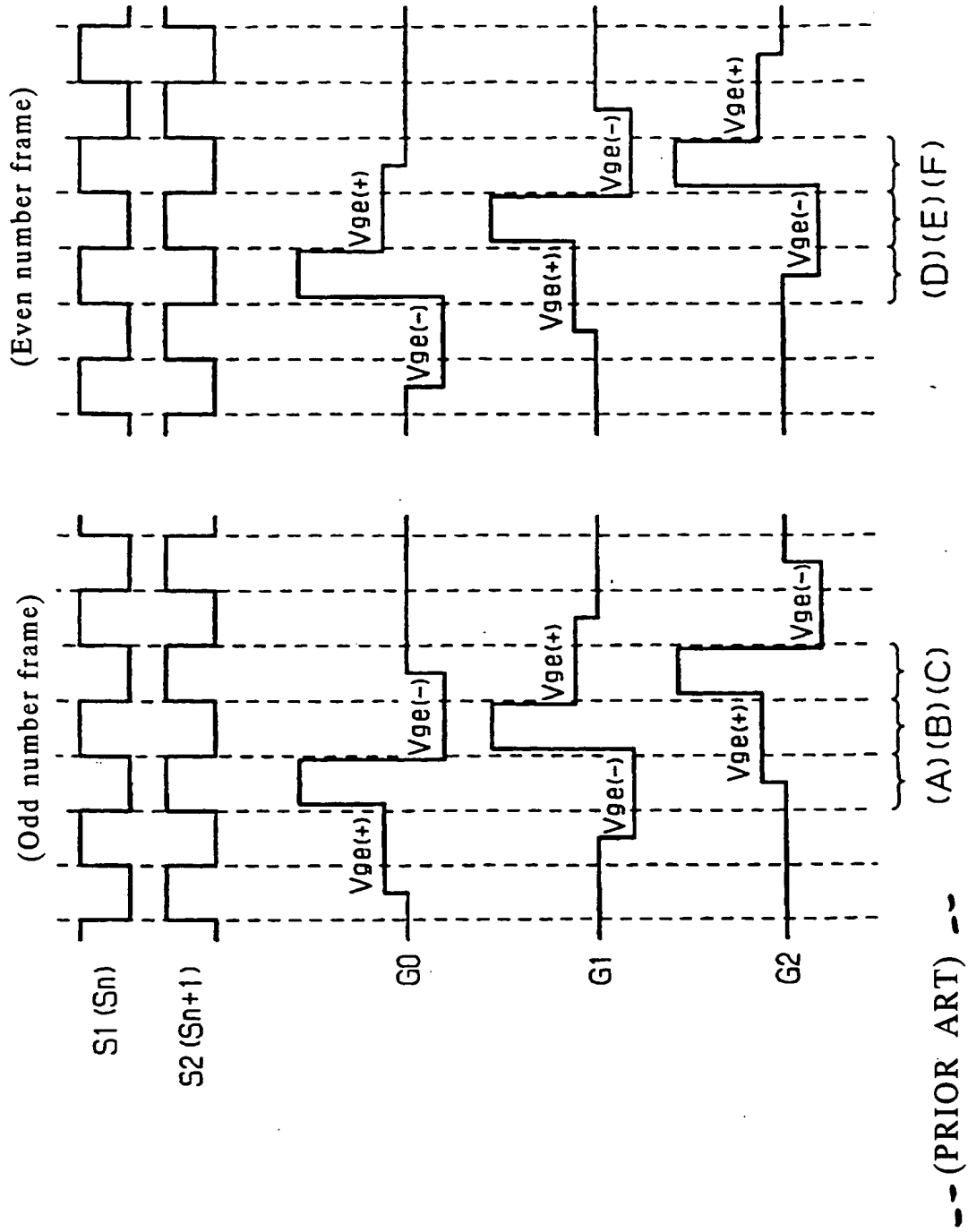


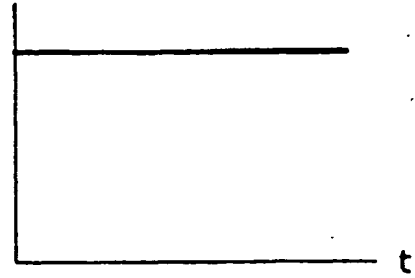
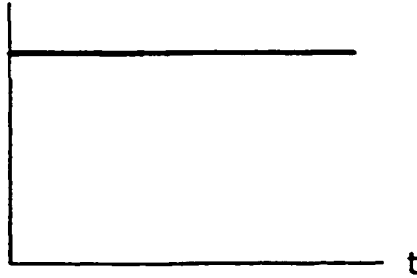
FIG. 39

(Power feeding edge)

(Terminal edge)

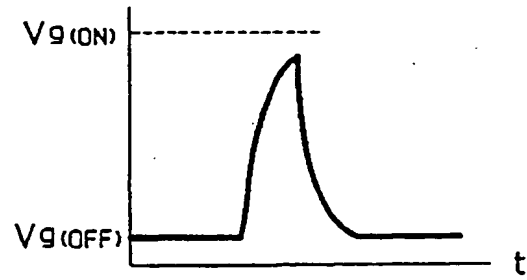
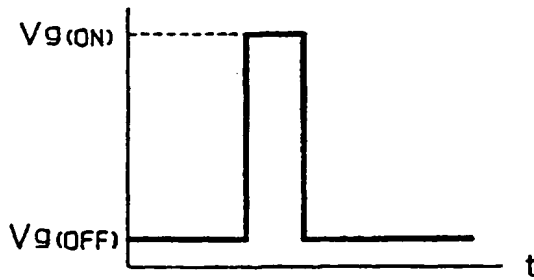
Source voltage

Source voltage



Gate voltage

Gate voltage



Pixel voltage

Pixel voltage

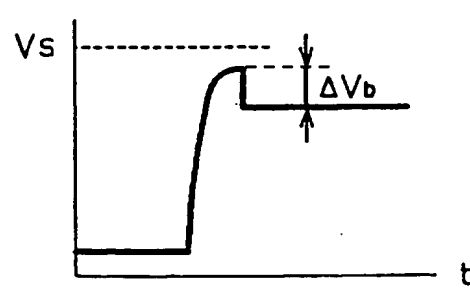
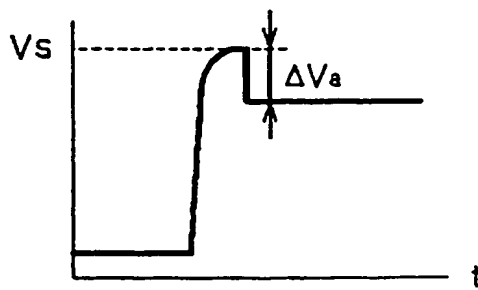


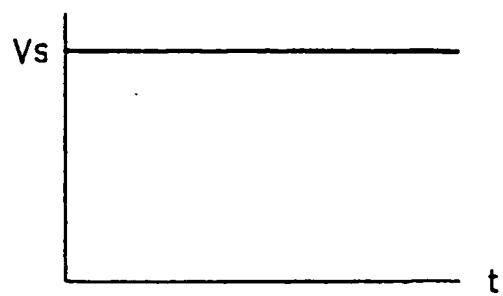
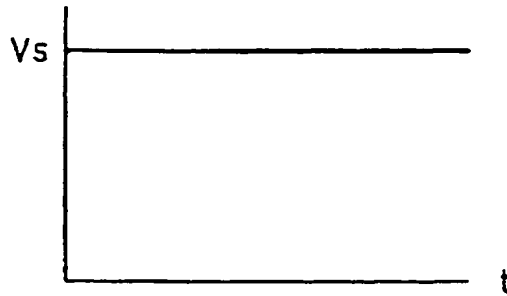
FIG. 40 --(PRIOR ART)--

(Power feeding edge)

(Terminal edge)

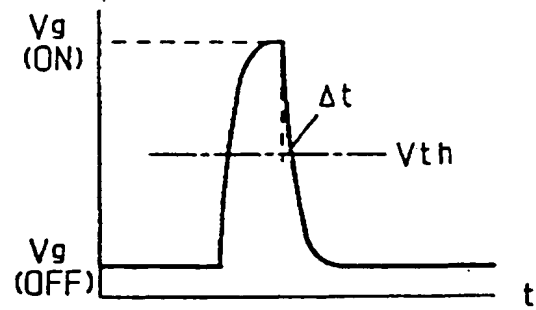
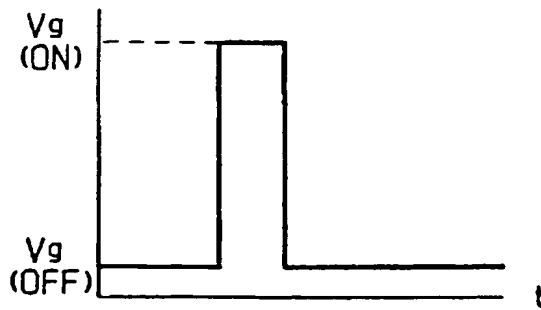
Source voltage

Source voltage



Gate voltage

Gate voltage



Pixel voltage

Pixel voltage

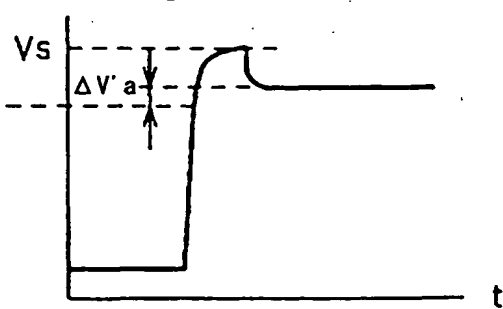
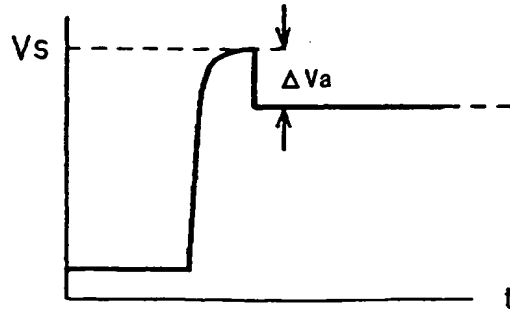


FIG. 41 - (PRIOR ART) --

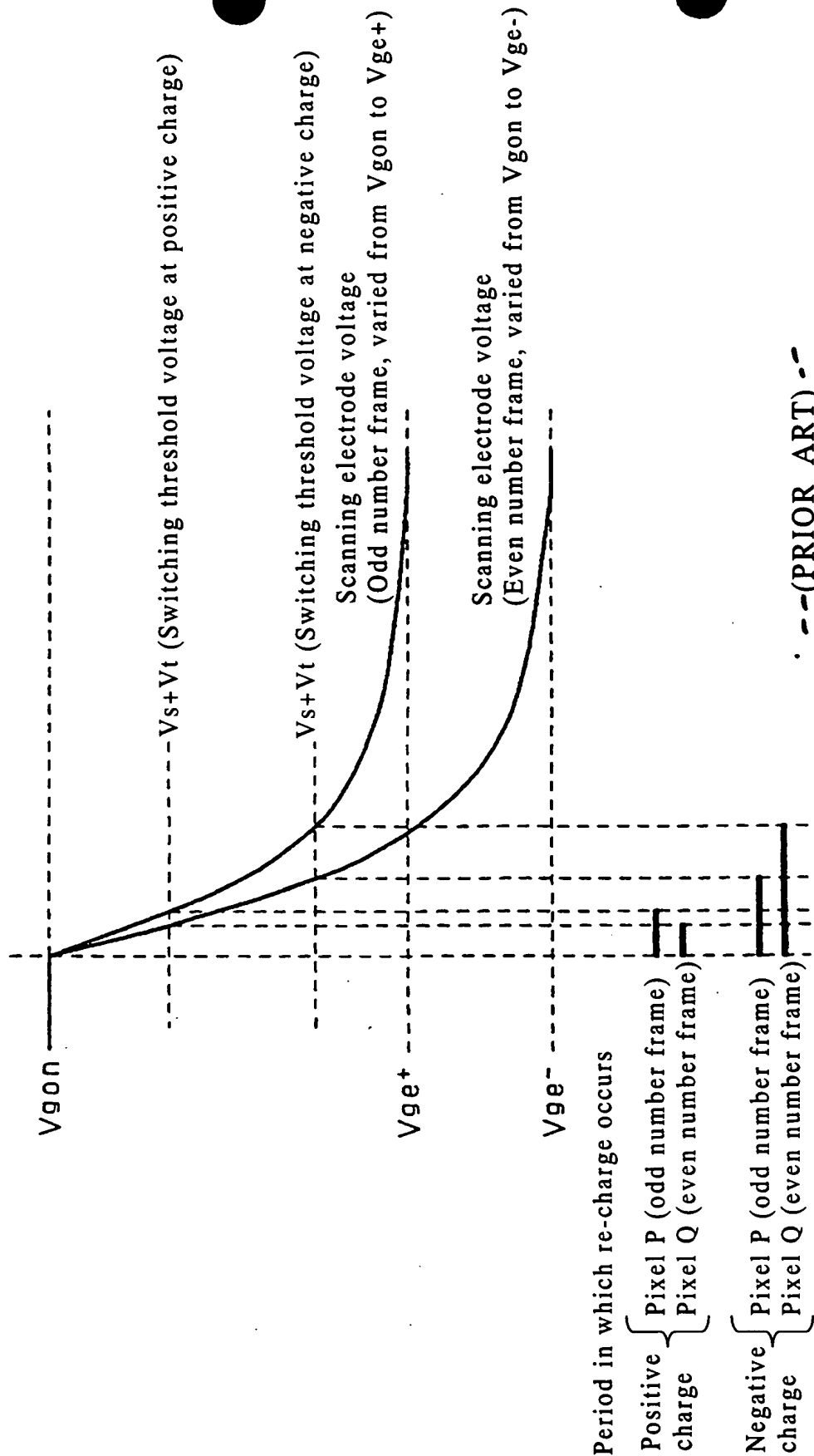


FIG. 42

(Power feeding edge)

(Terminal edge)

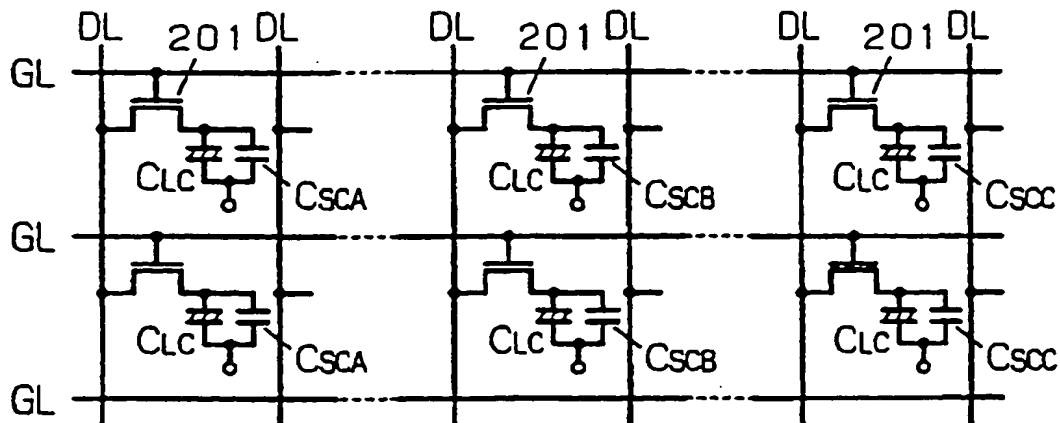
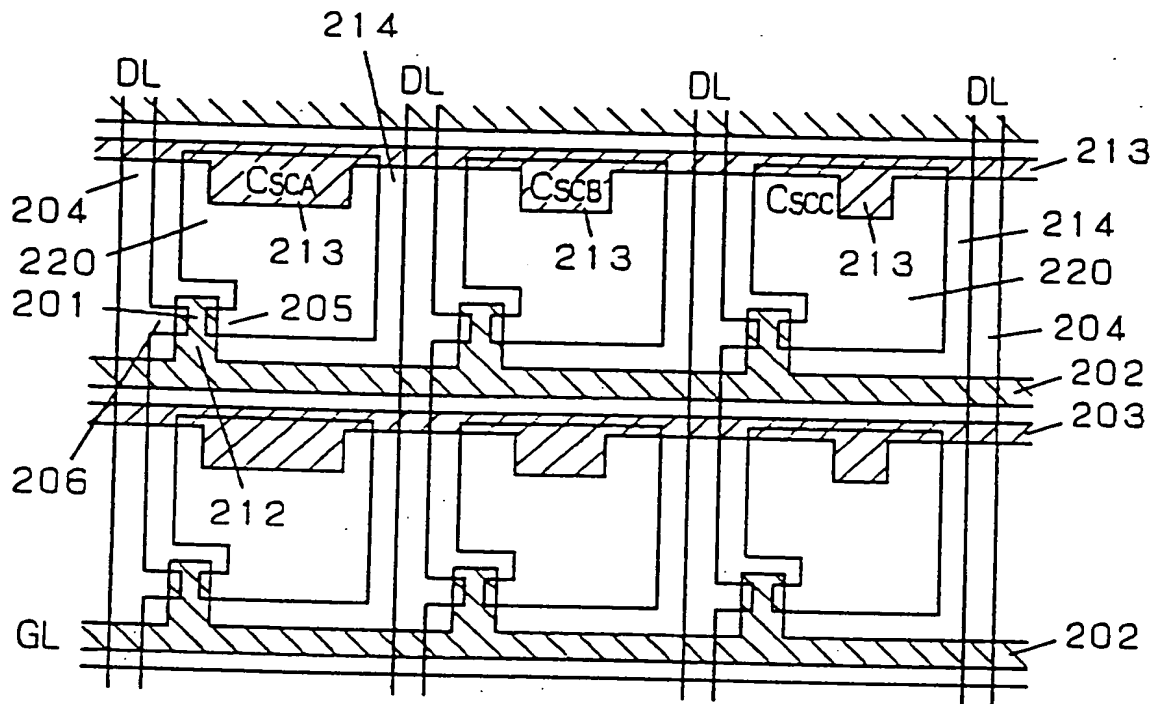


FIG. 43 --(PRIOR ART)--



-- (PRIOR ART) --

FIG. 44